

PATENT APPLICATION

FORM PTO 1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
10004625-1	09/823,596	6826
APPLICANT	Carl D. Burch	
FILING DATE	GROUP	
March 30, 2001	2183	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
<i>W</i>	1A	US-6,539,471 B2	03-2003	Sheaffer, Gad S.	712/217	
<i>W</i>	1B	US-5,701,442	12-1997	Ronen, Ronny	712/37	
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1I					
	1J					
	1K					


OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>W</i>	1L	Aho et al., "Compilers Principles, Techniques, and Tools," Addison-Wesley, 1986, pp. 530-531
<i>W</i>	1M	Thakkar et al., "The Internet Streaming SIMD Extensions," Intel Technology Journal, Q2, 1999, pg. 1 - 8
<i>W</i>	1N	Ryan and Thompson, "PowerPC 604 Weighs In," http://www.byte.com/art/9406/sec11/art1.htm , June 1994
<i>W</i>	1O	J. Smith and A. Pleszkun, "Implementing Precise Interrupts In Pipelined Processors," IEEE Trans. On Comp., Vol. 37, No. 5, May 1988, pp. 562-573
	1P	

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<i>N</i>	1M	A. Tannenbaum, "Structured Computer Organization," Prentice-Hall, 1984, pp. 10-12
<i>W</i>	1N	Aho et al., "Compilers Principles, Techniques, and Tools," Addison-Wesley, 1986, pp. 530-531
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
g2	AA	5,748,934	May 5, 1998	G. Lesartre, et al.	395/392	
g2	AB	5,799,167	Aug. 25, 1998	G. Lesartre	395/394	
g2	AC	5,809,275	Sept. 15, 1998	G. Lesartre	395/392	
g2	AD	5,838,944	Nov. 17, 1998	D. Kipp, et al.	395/394	

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		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	AE					
	AF					
	AG					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

g2	AH	K. M. Lepak, et al., "On The Value Locality of Store Instructions", 06/12-14/2000, 27th Annual International Symposium on Computer Architecture, pp. 1 - 10
g2	AI	M. Johnson, "Superscalar Microprocessor Design", 1991, Prentice Hall series in innovative technology, ISBN 0-13-875634-1, pp. 9 - 30
	AJ	
	AK	
	AL	

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